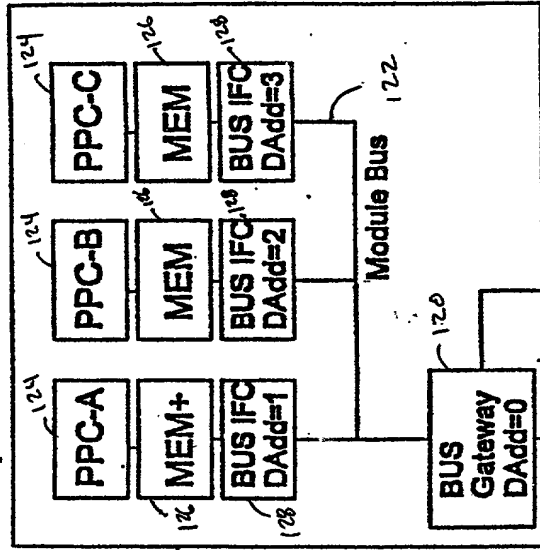
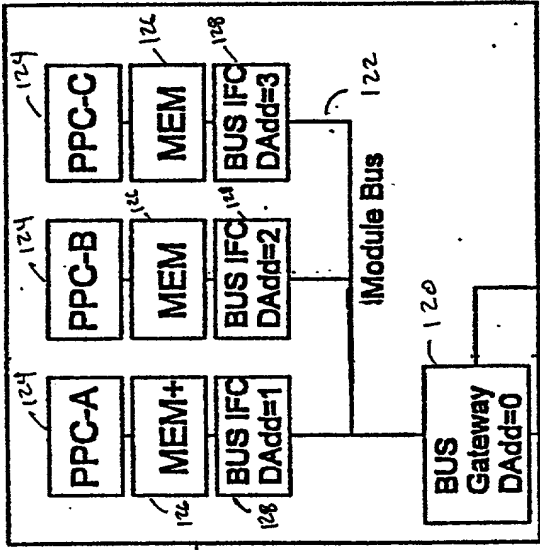


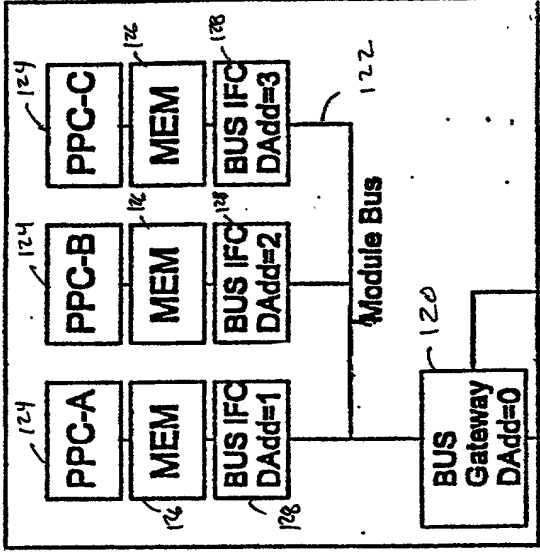
Temporal Processor Module A



Temporal Processor Module B

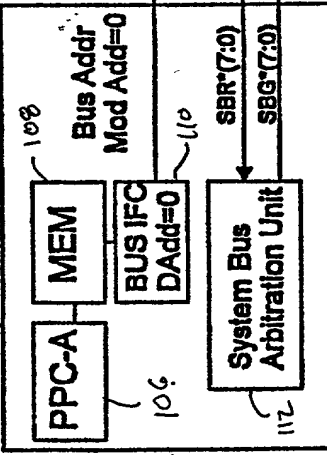


Temporal Processor Module C



System Bus

System Controller



Sensor Interface

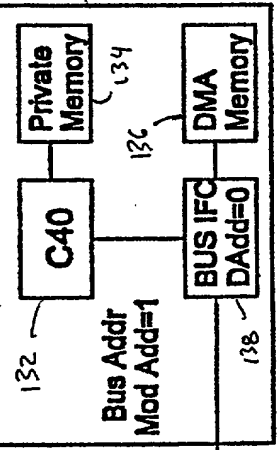


FIGURE 1

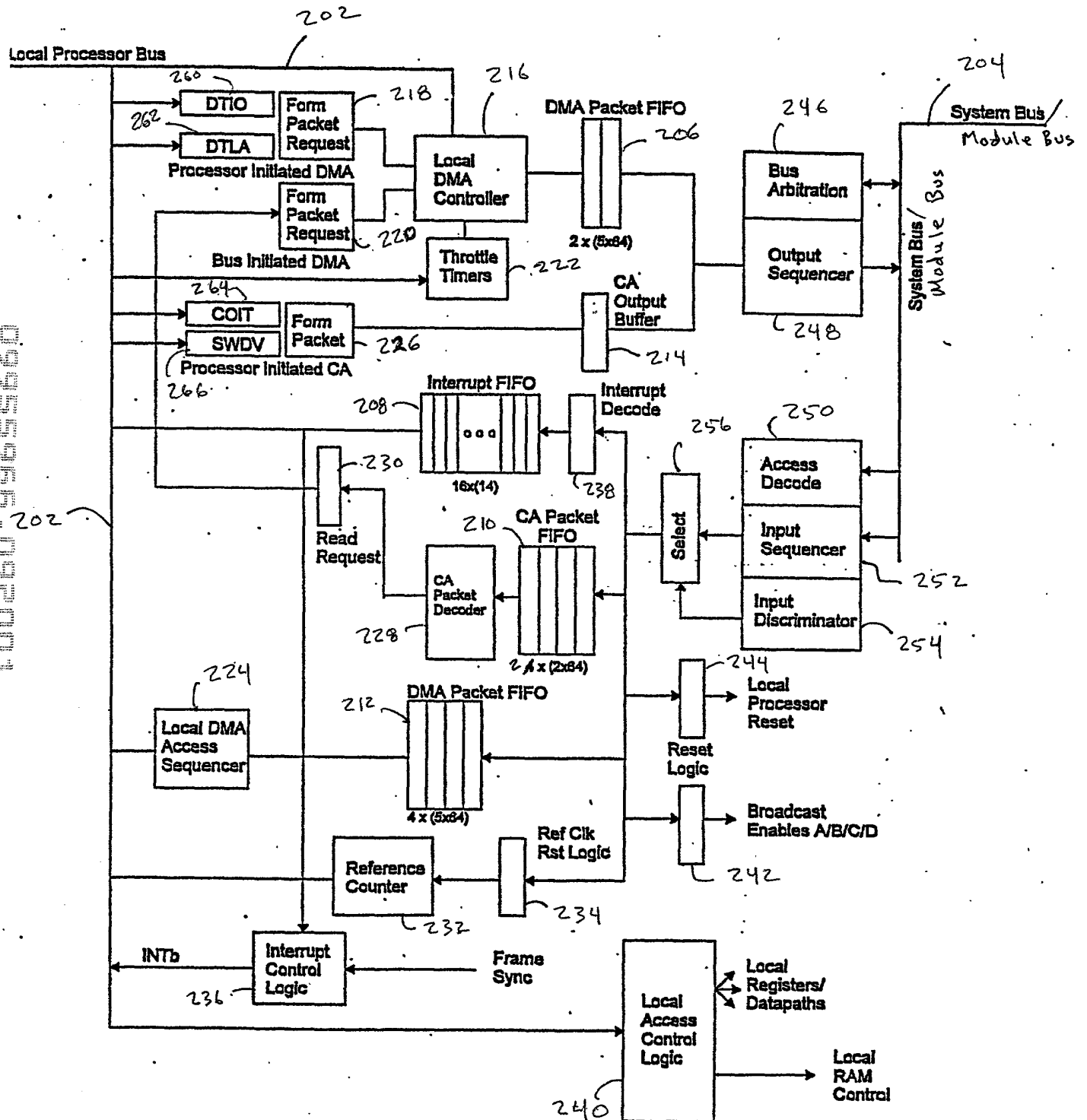


FIGURE 2

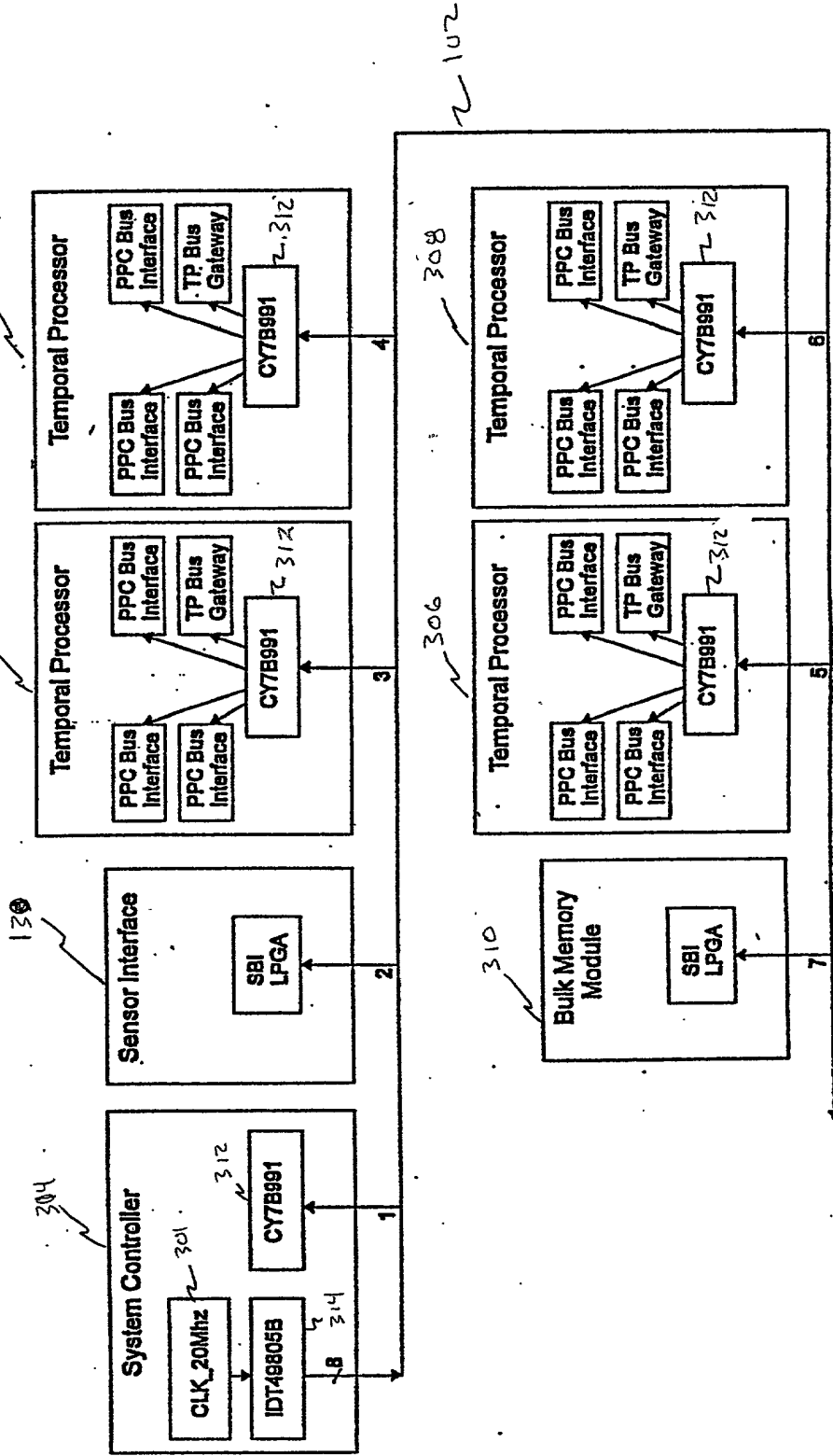


FIG. 3